

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No : Not Yet Assigned
Applicants : Hiroshi KATSUNAGA and Hiroshi MIYAGI
Filed : Herewith
For : AM INTERMEDIATE FREQUENCY VARIABLE GAIN AMPLIFIER
CIRCUIT, VARIABLE GAIN AMPLIFIER CIRCUIT AND ITS
SEMICONDUCTOR INTEGRATED CIRCUIT
Art Unit : Not Yet Assigned
Examiner : Not Yet Assigned
International
Application No.: PCT/JP2004/016774

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Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Applicants respectfully request that prior to examination, the claims of the above-identified application be amended as shown hereafter. More specifically, Claim 4 has been amended for the first time.

Amendments to the Claims are reflected in the listing of claims which begin on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.